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## \*BIBDATASHEET\*

CONFIRMATION NO. 9619

Bib Data Sheet

SERIAL NUMBER 10/600,568	FILING DATE 06/23/2003  RULE	CLASS 257	GROUP ART UNIT 2814	ATTORNEY DOCKET NO. 04329.2444-01
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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/677,743 10/03/2000 PAT 6,611,060

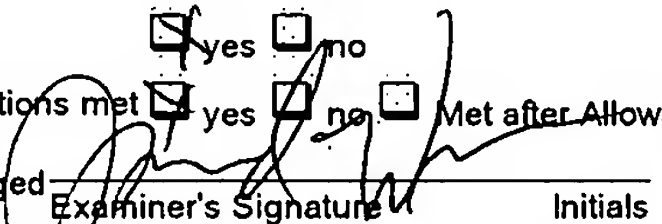
## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 11-282404 10/04/1999

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## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 09/12/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged  Examiner's Signature Initials	JAPAN	8	12	1

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## TITLE

Method of manufacturing semiconductor device and semiconductor device

including Two-step Polishing operation for  
 (cap metal)

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
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